QUICKSWITCH® PRODUCTS HIGH-SPEED 32-BIT BUS EXCHANGE SWITCH IN MILLIPAQ™

IDTQS34X383

FEATURES:

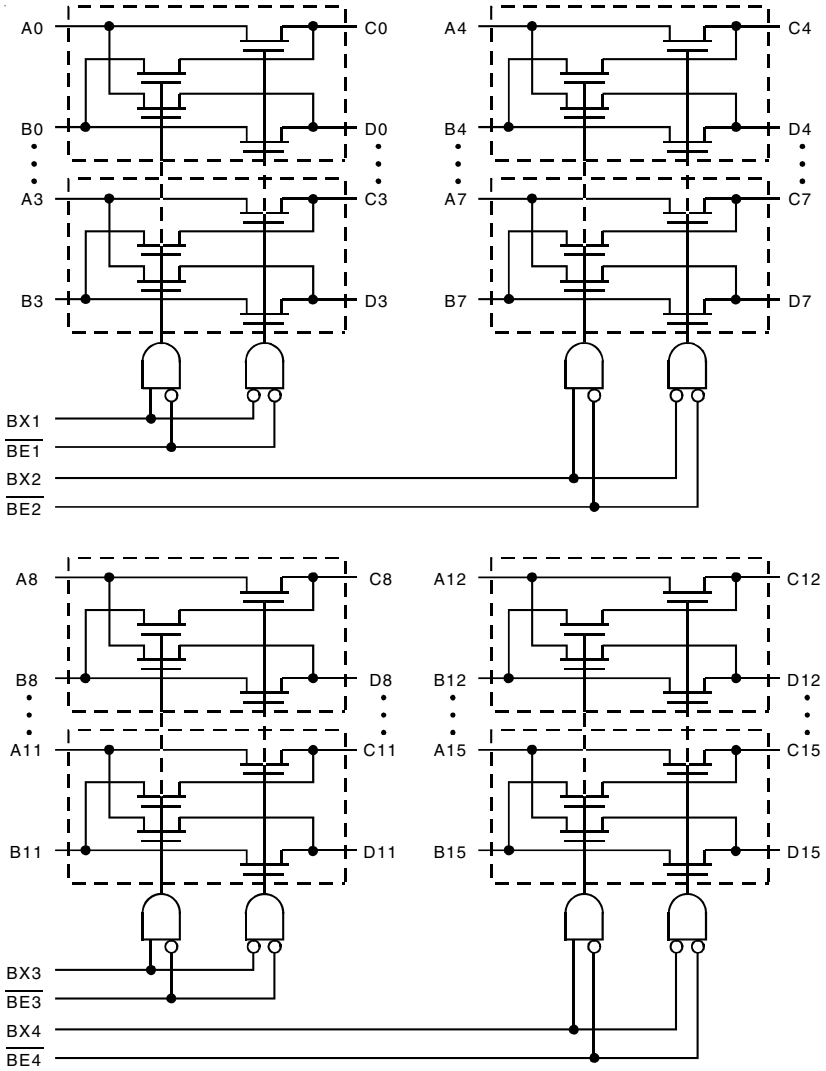
- **5Ω switches connect inputs to outputs**
- **Zero propagation delay**
- **Direct bus connect**
- **Live insertion capability**
- **Low power CMOS proprietary technology**
- **Bus exchange allows nibble swap**
- **TTL-compatible control inputs**
- **Available in 80-pin Millipaq package**

DESCRIPTION:

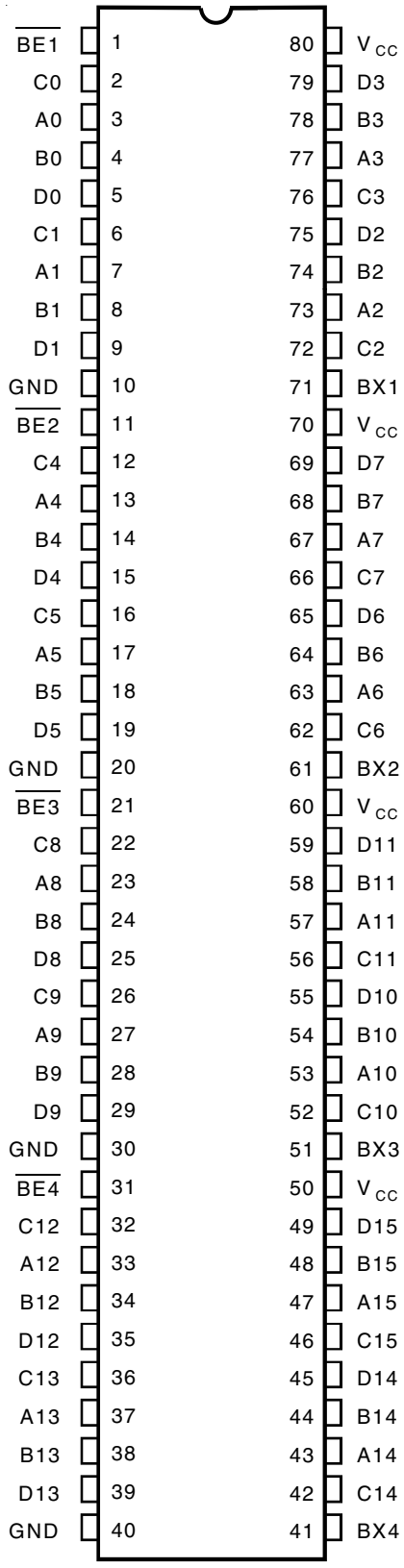
The QS34X383 provides four sets of eight high-speed CMOS TTL-compatible bus switches. The low ON resistance (5Ω) of the QS34X383 allows inputs to be connected to outputs without adding propagation delay and without generating additional ground bounce noise. The Bus Enable ($\overline{BEx}$) signals turn the switches on. The Bus Exchange (BXx) signals provide nibble swap of the AB and CD pairs of signals. This exchange configuration allows byte swapping of buses in systems. It can also be used as a 16-bit 2-to-1 multiplexer and to create low delay barrel shifters, etc.

The QS34X383 is characterized for operation at -40°C to +85°C.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATION



MILLIPAQ
TOP VIEW

ABSOLUTE MAXIMUM RATINGS(1)

Symbol	Description	Max	Unit
V_{TERM}(2)	Supply Voltage to Ground	–0.5 to +7	V
V_{TERM}(3)	DC Switch Voltage Vs	–0.5 to +7	V
V_{TERM}(3)	DC Input Voltage V_{IN}	–0.5 to +7	V
V_{AC}	AC Input Voltage (pulse width ≤ 20ns)	–3	V
I_{OUT}	DC Output Current	120	mA
P_{MAX}	Maximum Power Dissipation (T_A = 85°C)	1.4	W
T_{STG}	Storage Temperature	–65 to +150	°C

NOTE:
1. Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
2. Vcc terminals.
3. All terminals except Vcc.

CAPACITANCE

(T_A = +25°C, f = 1.0MHz, V_{IN} = 0V, V_{OUT} = 0V)

Pins	Max.(1)	Unit
Control Pins	8	pF
Quickswitch Channels (Switch OFF)	8	pF

NOTE:
1. This parameter is measured at characterization but not tested.

PIN DESCRIPTION

Pin Names	I/O	Description
Ax, Bx	I/O	Buses A, B
Cx, Dx	I/O	Buses C, D
$\overline{BE}$x	I	Bus Switch Enable
BXx	I	Bus Exchange

FUNCTION TABLE(1)

$\overline{BE}$x	BXx	Ax	Bx	Function
H	X	Z	Z	Disconnect
L	L	Cx	Dx	Connect
L	H	Dx	Cx	Exchange

NOTE:
1. H = HIGH Voltage Level
   L = LOW Voltage Level
   X = Don't Care
   Z = High-Impedance

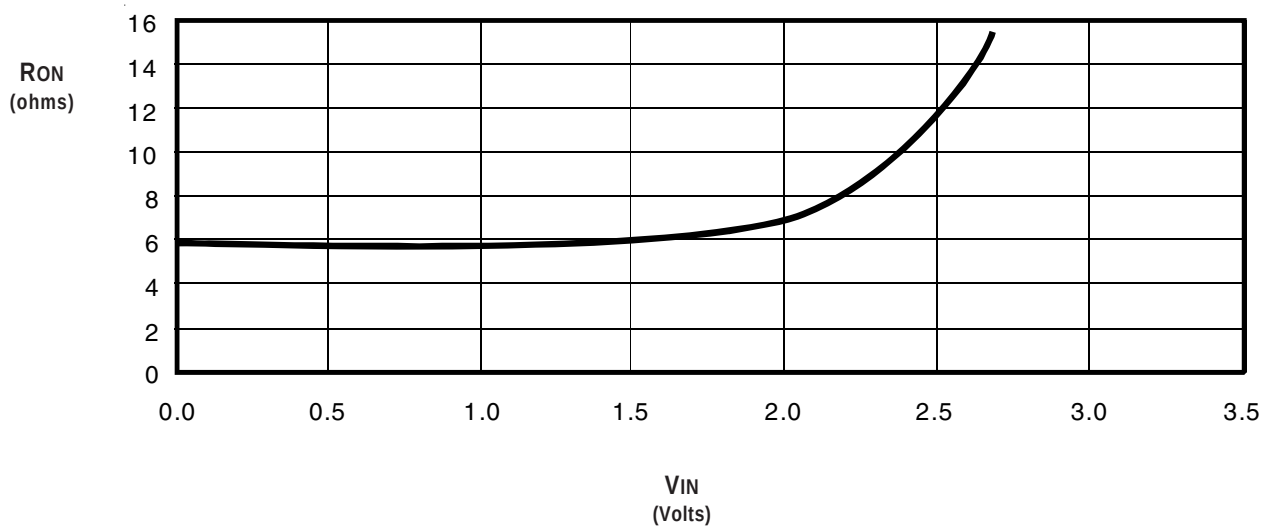
DC ELECTRICAL CHARACTERISTICS OVER OPERATING RANGE

Following Conditions Apply Unless Otherwise Specified:
Industrial: T_A = –40°C to +85°C, V_{CC} = 5.0V ±5%

Symbol	Parameter	Test Conditions	Min.	Typ.[1]	Max.	Unit
V_{IH}	Input HIGH Level	Guaranteed Logic HIGH for Control Pins	2	—	—	V
V_{IL}	Input LOW Level	Guaranteed Logic LOW for Control Pins	—	—	0.8	V
I_{IN}	Input LeakageCurrent (Control Inputs)	0V ≤ V_{IN} ≤ V_{CC}, Control Inputs	—	—	±5	µA
I_{OZ}	Off-State Output Current (Hi-Z)	0V ≤ V_{OUT} ≤ V_{CC}, Switches OFF	—	—	±5	µA
R_{ON}	Switch ON Resistance[2,3]	V_{CC} = Min., V_{IN} = 0V, I_{ON} = 30mA	—	6	8	Ω
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} =15mA	—	12	17	

NOTES:
1. Typical values are at V_{CC} = 5.0V, T_A = 25°C.
2. Max value of R_{ON} is guaranteed but not production tested.
3. Measures by voltage drop between the AB and CD pin at the indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A or B, C or D) pins.

TYPICAL ON RESISTANCE vs V_{IN} AT V_{CC} = 5V



POWER SUPPLY CHARACTERISTICS

Symbol	Parameter	Test Conditions[1]	Max.	Unit
I_{CCQ}	Quiescent Power Supply Current	V_{CC} = Max., $\overline{BEx}$ = GND or Vcc, f = 0	6	mA
ΔI_{CC}	Power Supply Current per Control Input HIGH [2]	V_{CC} = Max., $\overline{BEx}$ = 3.4V, f = 0	2.5	mA
I_{CCD}	Dynamic Power Supply Current per MHz[3]	V_{CC} = Max., A and B pins open Control Inputs Toggling at 50% Duty Cycle	0.25	mA/MHz

NOTES:
1. For conditions shown as Min. or Max., use the appropriate values specified under DC Electrical Characteristics.
2. Per TLL driven input (V_{IN} = 3.4V, control inputs only). A-D pins do not contribute to ΔI_{CC}.
3. This current applies to the control inputs only and represents the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is guaranteed but not production tested.

SWITCHING CHARACTERISTICS OVER OPERATING RANGE

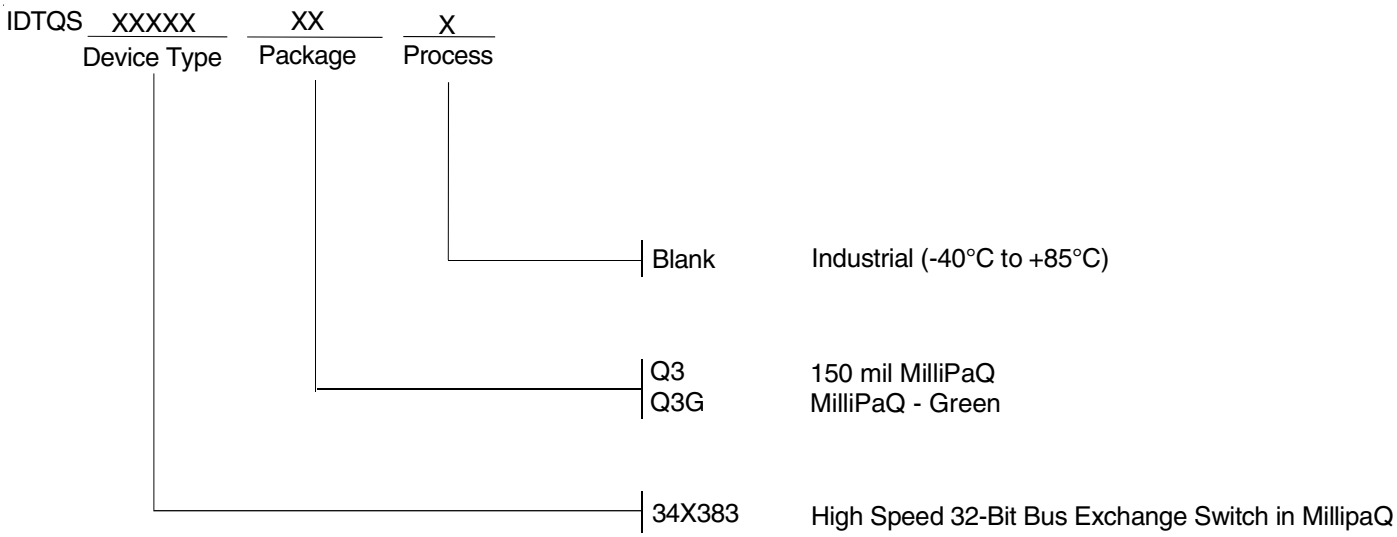
T_A = -40°C to +85°C, V_{CC} = 5.0V ± 5%;

C_{LOAD} = 50pF, R_{LOAD} = 500Ω unless otherwise noted.

Symbol	Parameter	Min.[1]	Typ.	Max.	Unit
t_{PLH} t_{PHL}	Data Propagation Delay[2,3] AxBx to CxDx, CxDx to AxBx	—	—	0.25	ns
t_{PZL} t_{PZH}	Switch Turn-on Delay $\overline{BEx}$ to Ax, Bx, Cx, Dx	1.5	—	6.5	ns
t_{PLZ} t_{PHZ}	Switch Turn-off Delay[2] $\overline{BEx}$ to Ax, Bx, Cx, Dx	1.5	—	5.5	ns
t_{BX}	Switch Multiplex Delay[2] BX to Ax, Bx, Cx, Dx	1.5	—	6.5	ns
Q_{CI}	Charge Injection[4,5]	—	1.5	—	pC

NOTES:
1. Minimums are guaranteed but not production tested.
2. This parameter is guaranteed but not production tested.
3. The bus switch contributes no propagation delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for C_L = 50pF. Since this time constant is much smaller than the rise and fall times of typical driving signals, it adds very little propagation delay to the system. Propagation delay of the bus switch, when used in a system, is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
4. Measured at switch turn off, A to C, load = 50pF in parallel with 10 meg scope probe, V_{IN} at I = 0V.
5. Measured at switch turn off through bus multiplexer, A to C ≥ A to D, B connected to C, load = 50pF in parallel with 10 meg scope probe, V_{IN} at A = 0V. Charge injection is reduced because the injection from the turn off of the A to C switch is compensated by the turn on of the B to C switch.

ORDERING INFORMATION



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